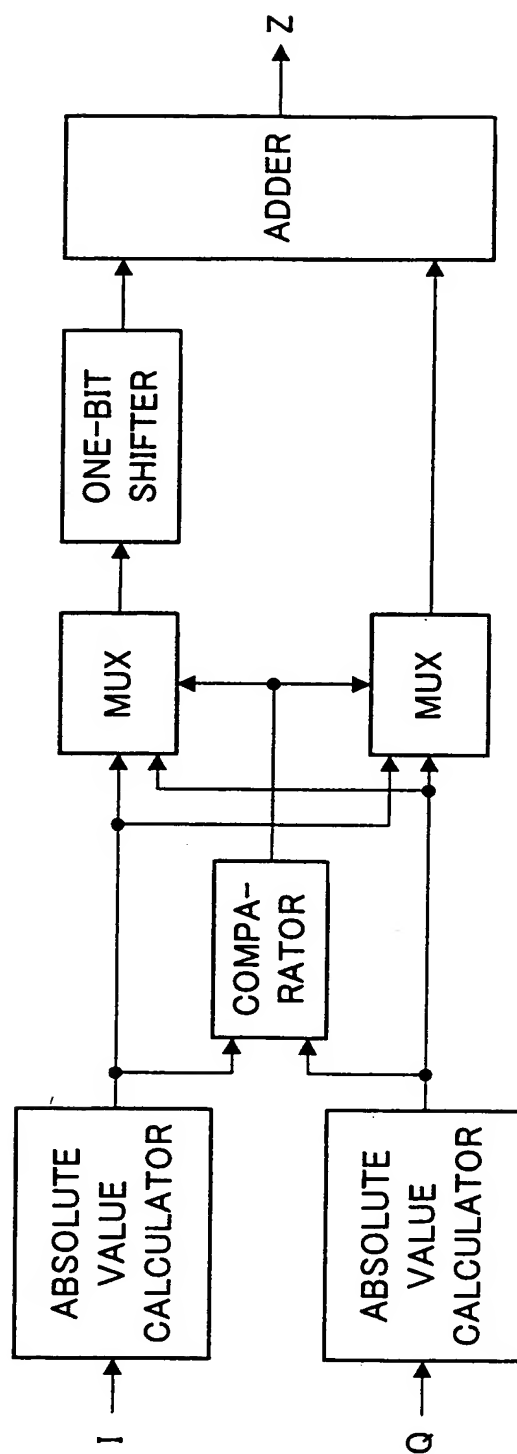


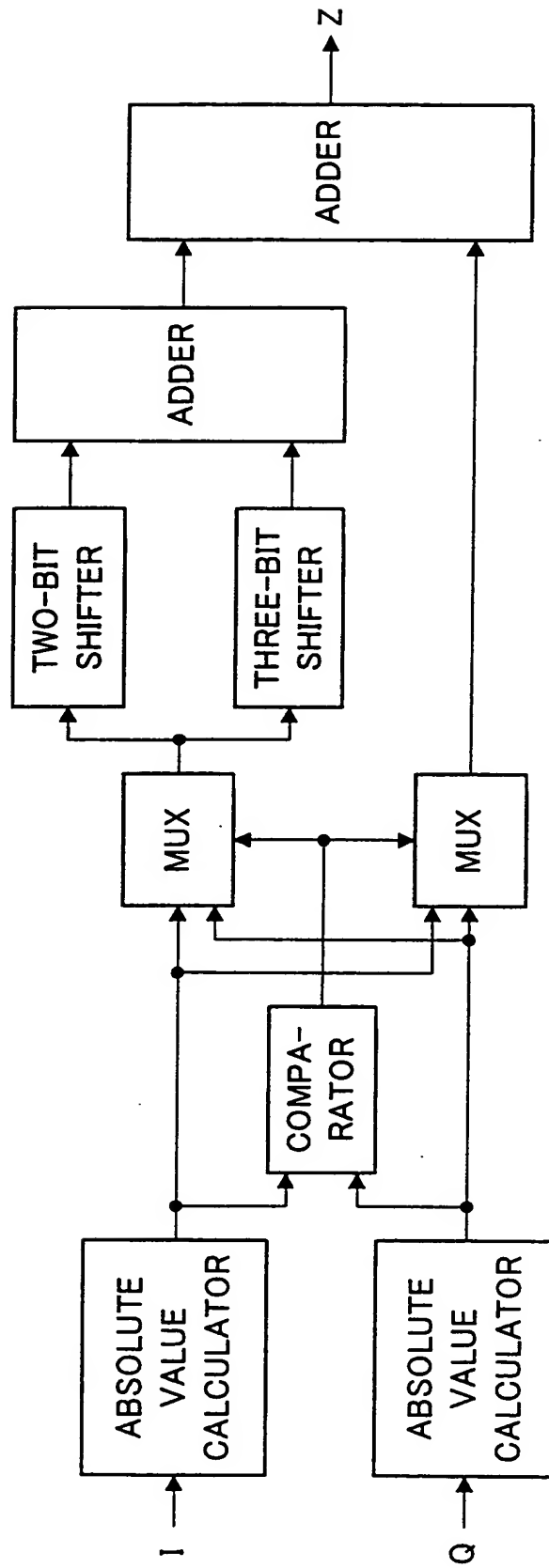
PRIOR ART

FIG.1



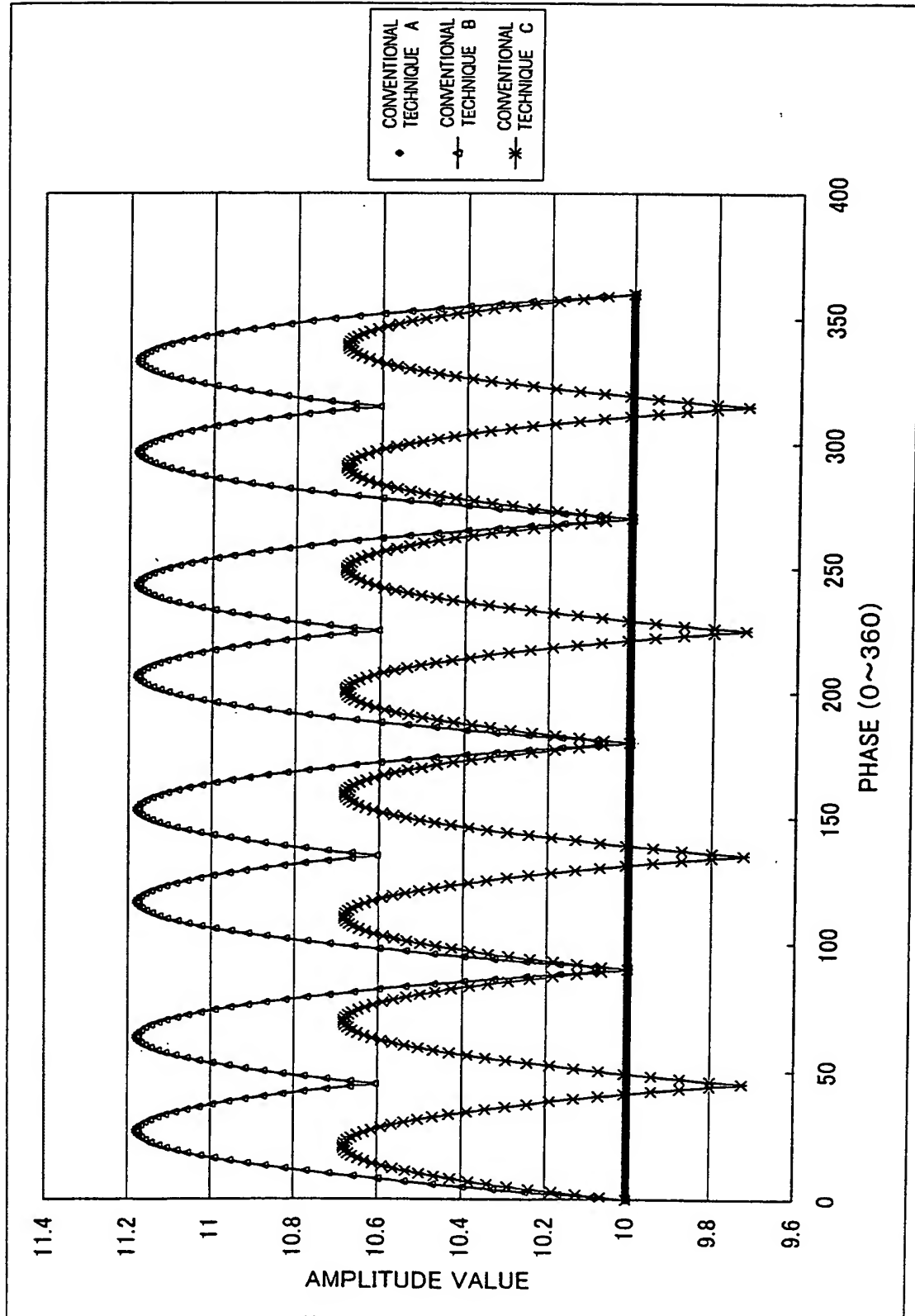
PRIOR ART

FIG.2



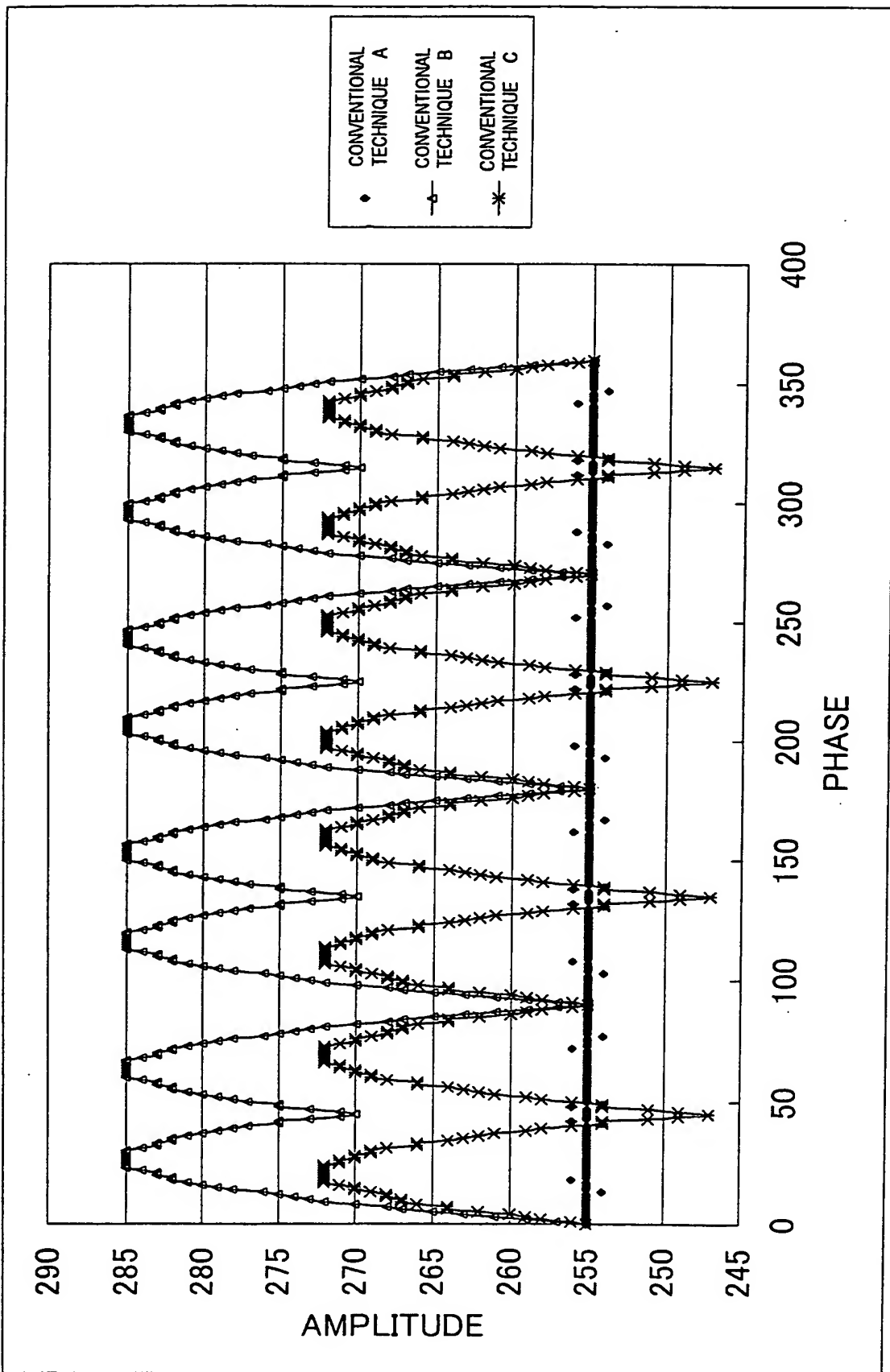
PRIOR ART

FIG.3



PRIOR ART

FIG.4



PRIOR ART

FIG.5

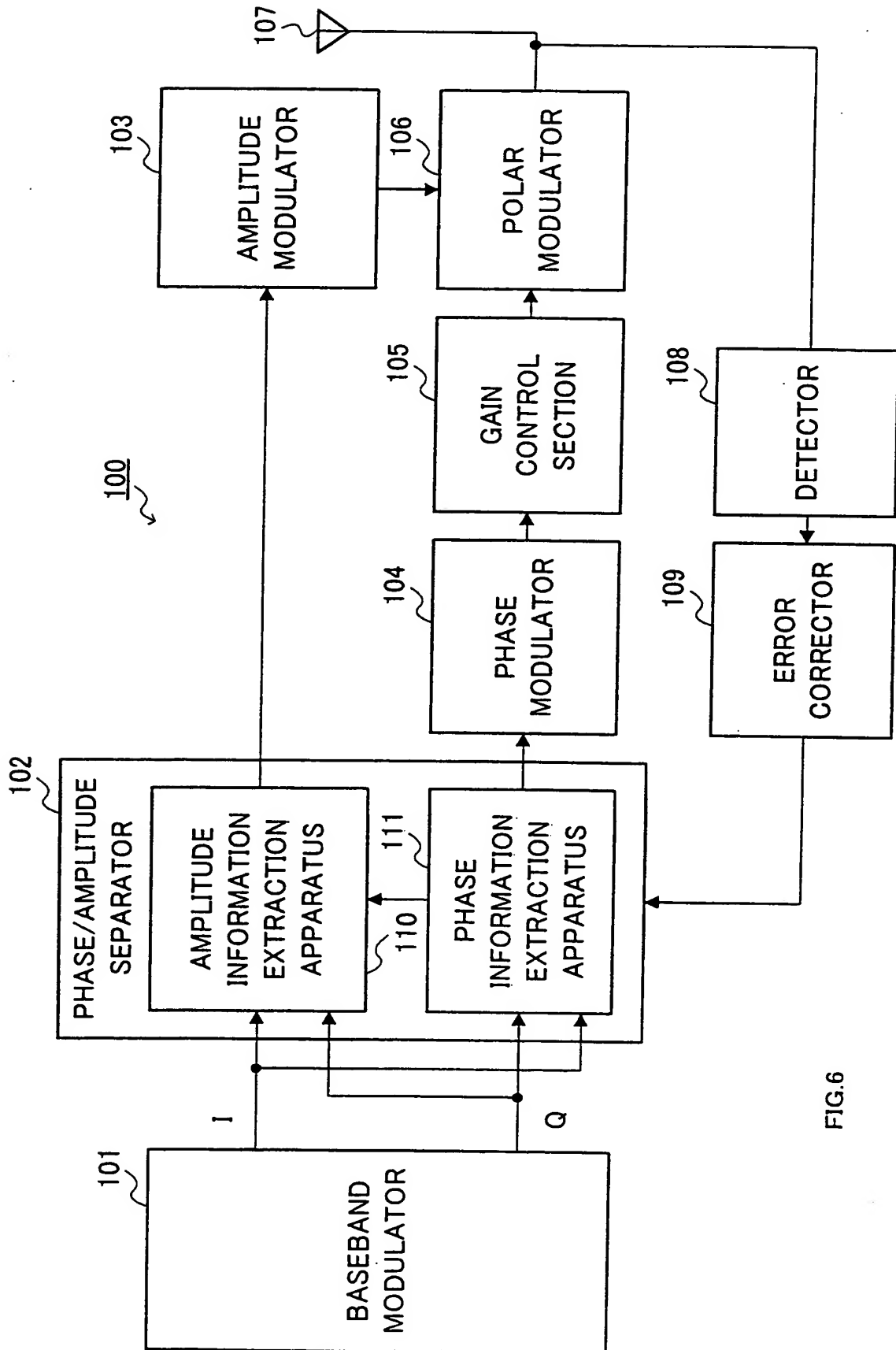


FIG. 6

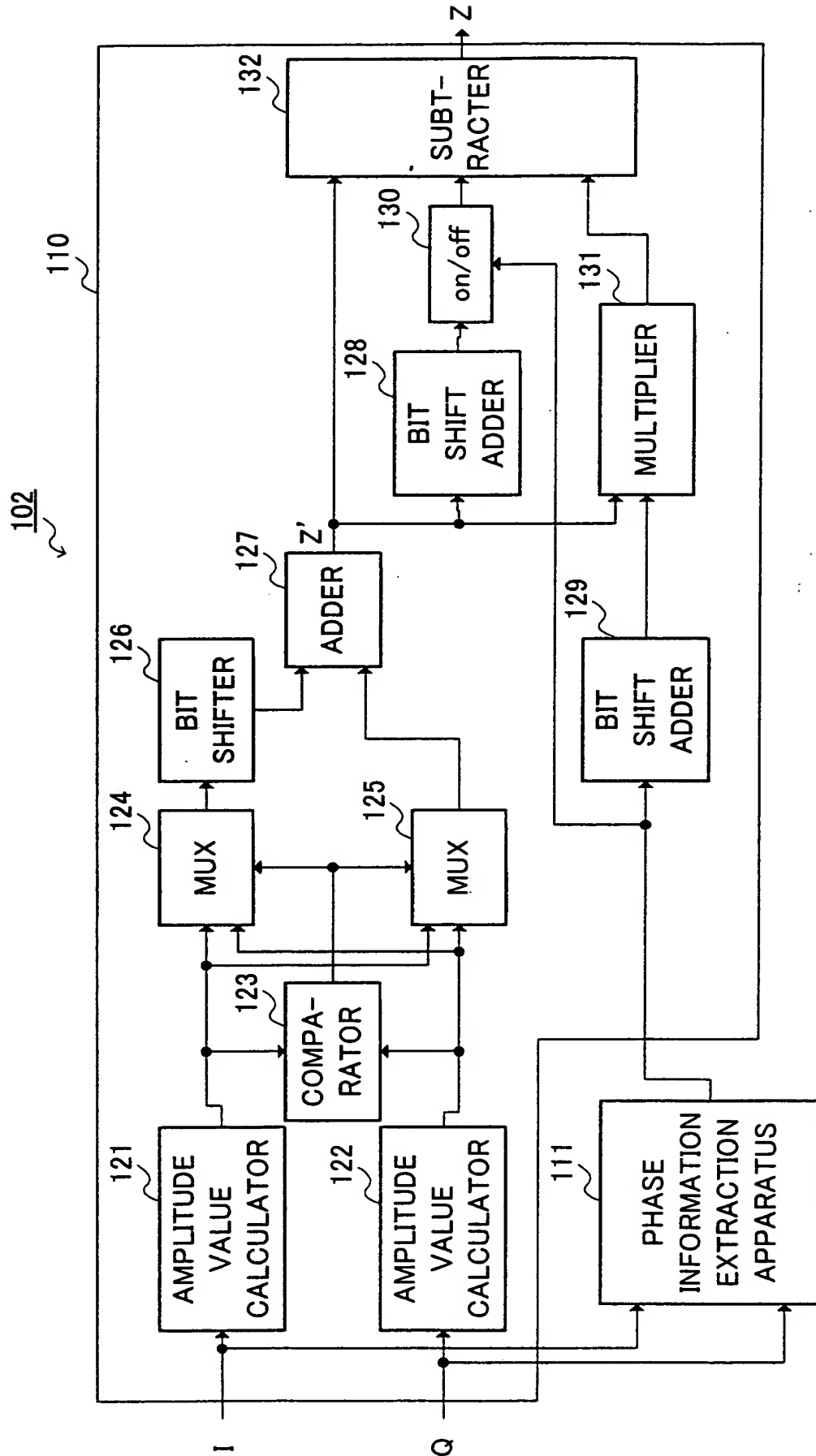


FIG.7

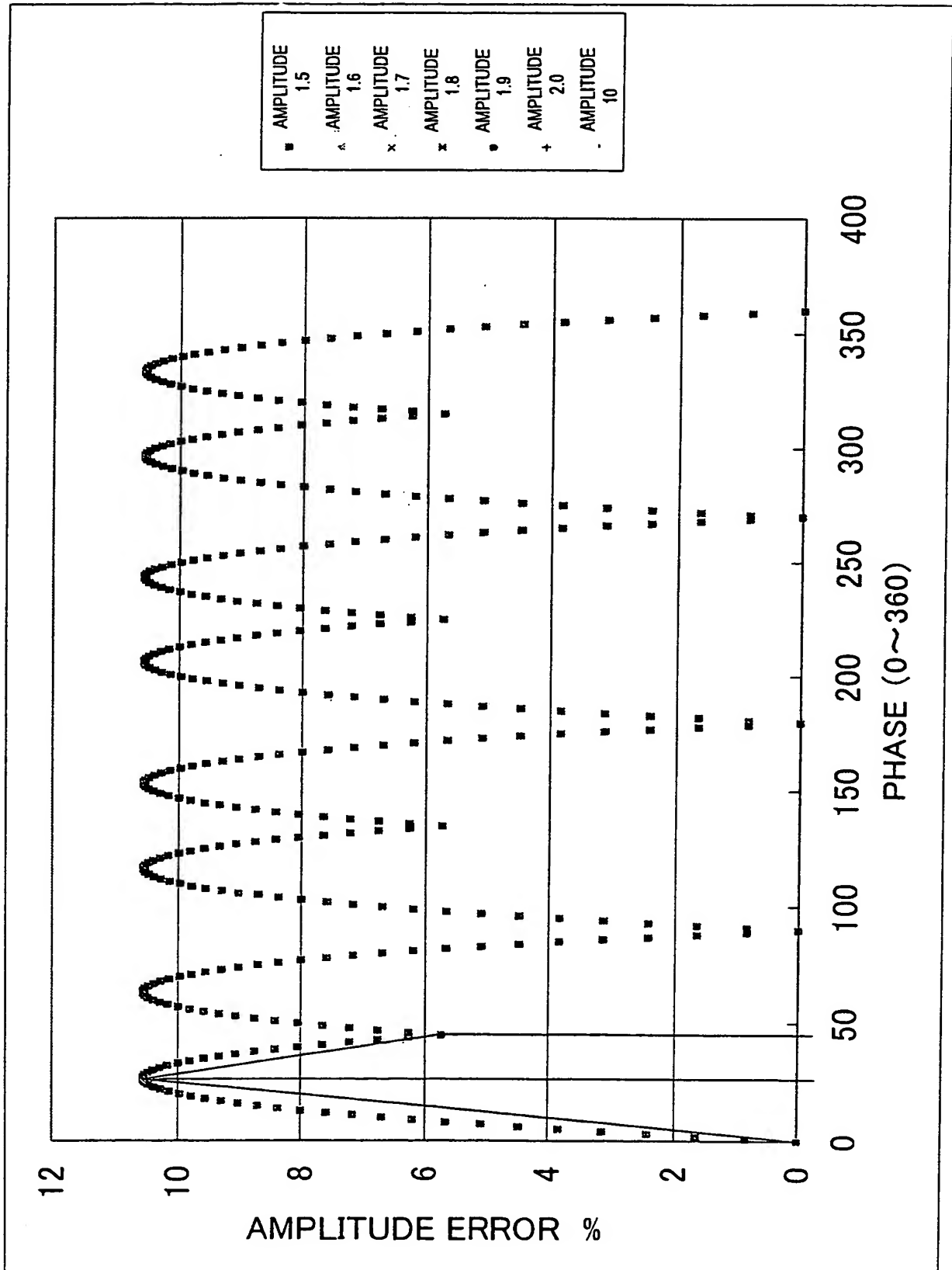


FIG.8

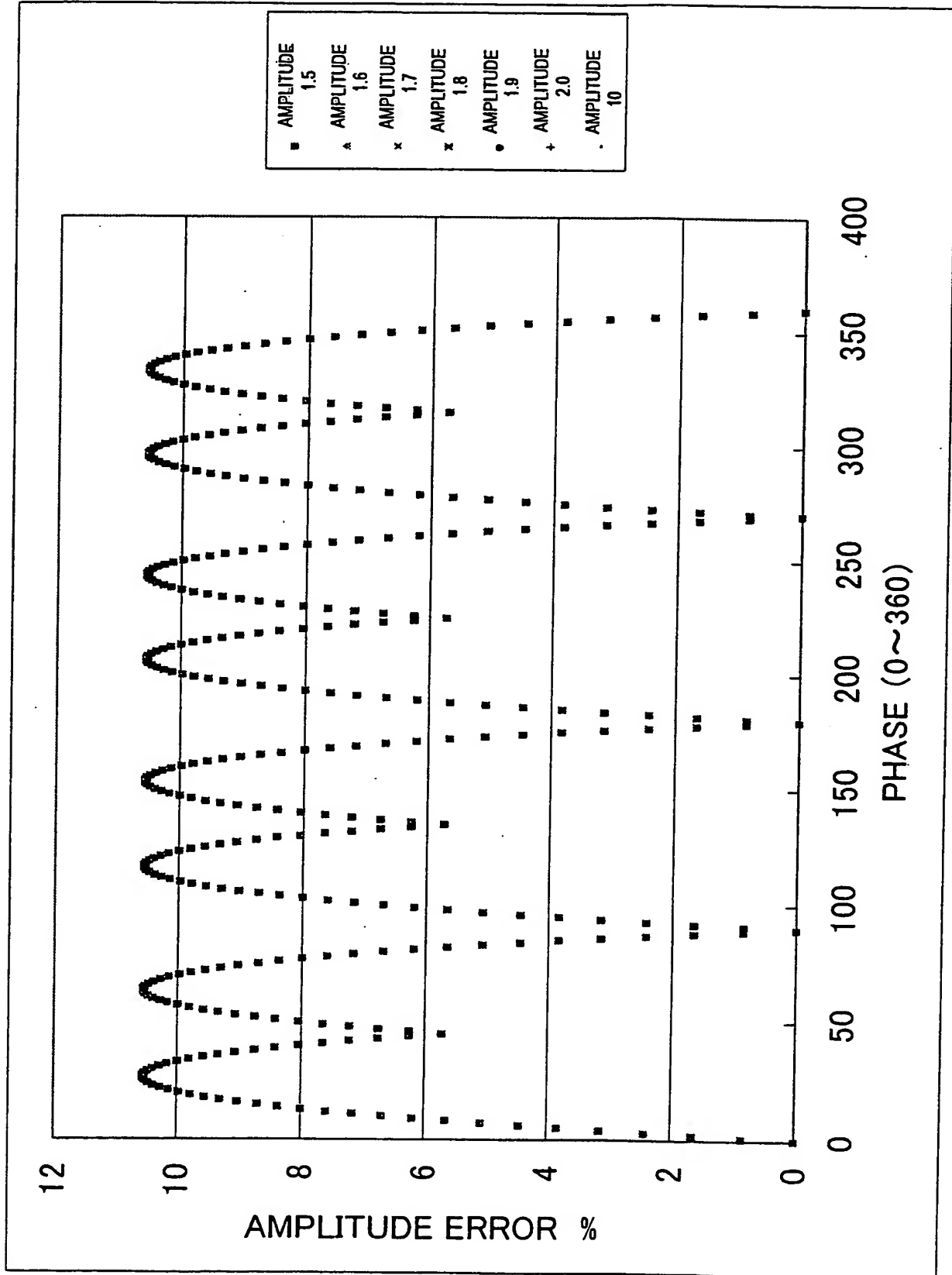


FIG.9

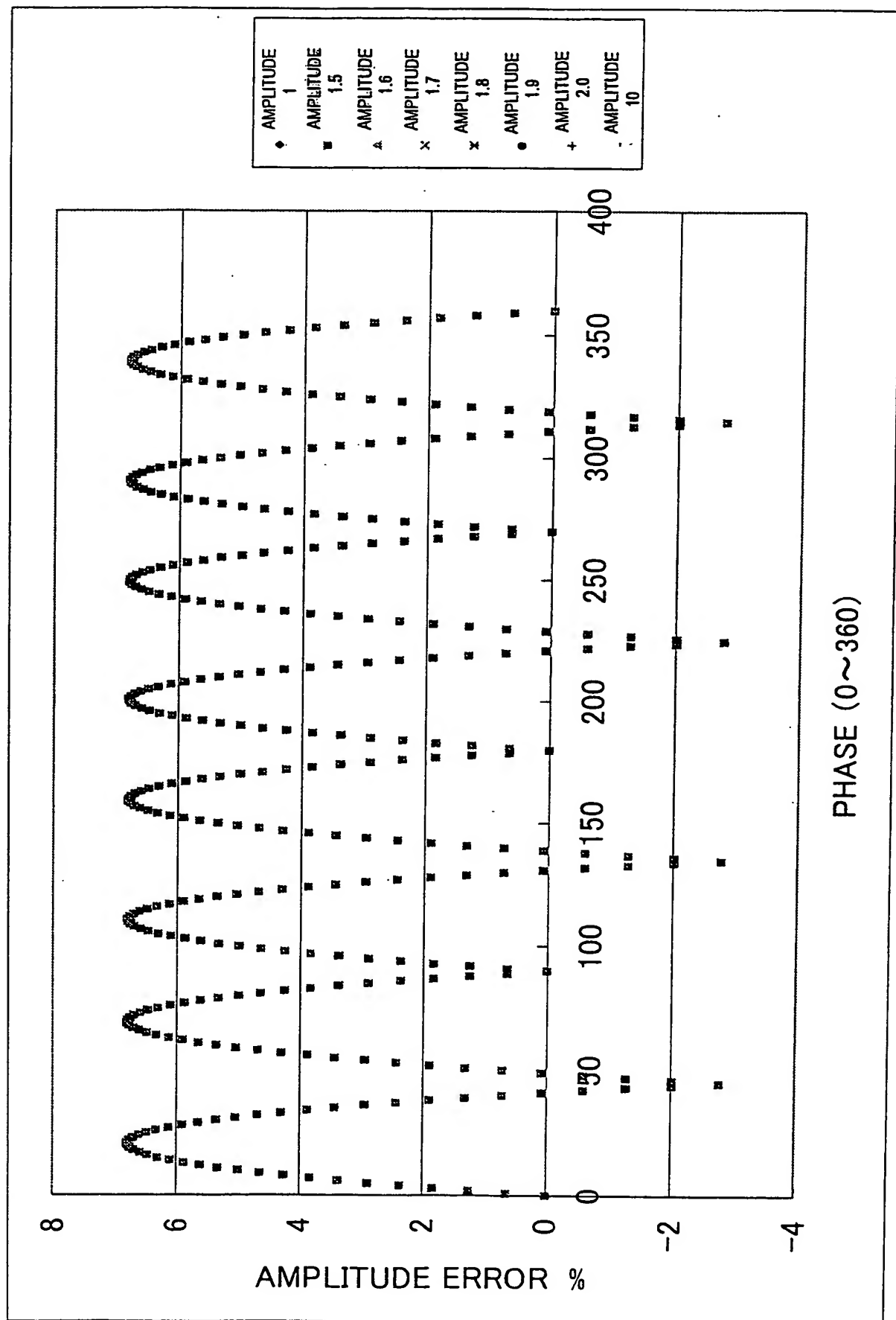


FIG.10

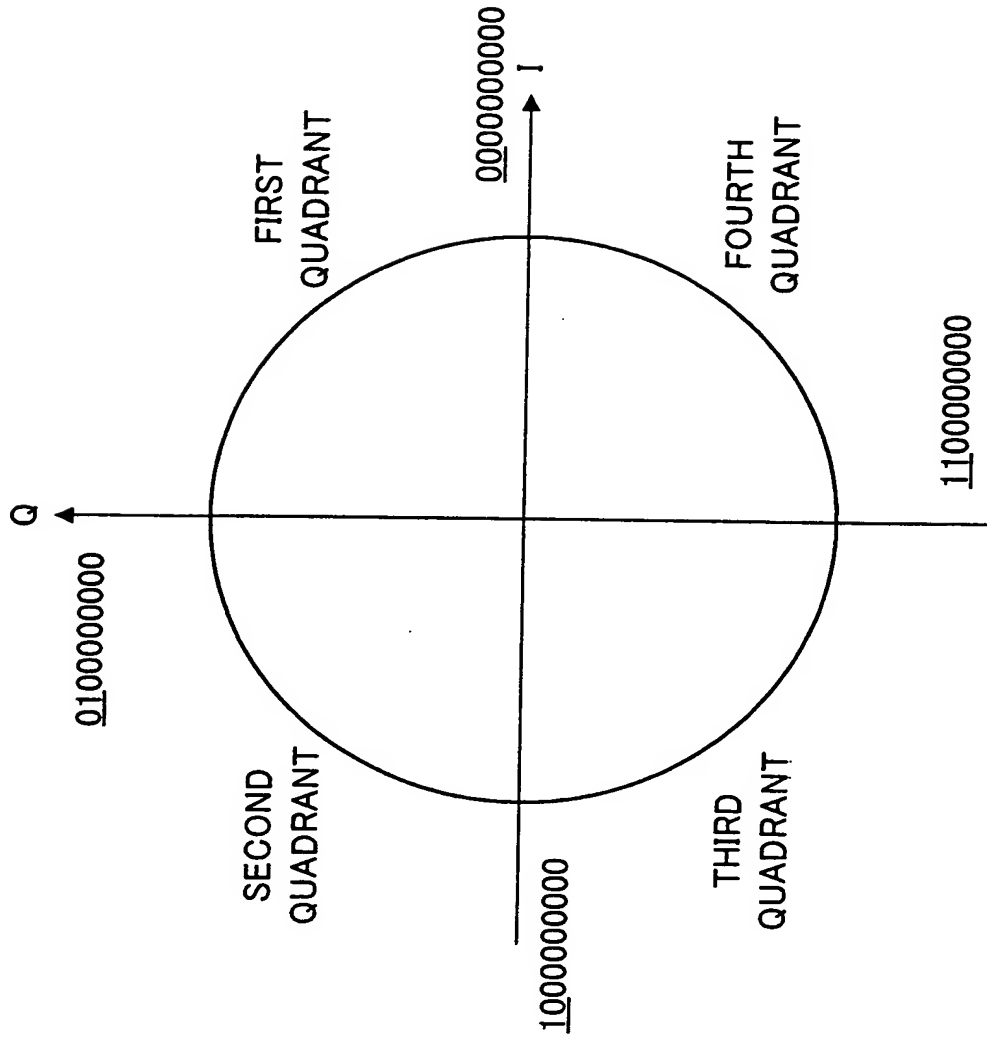


FIG.11

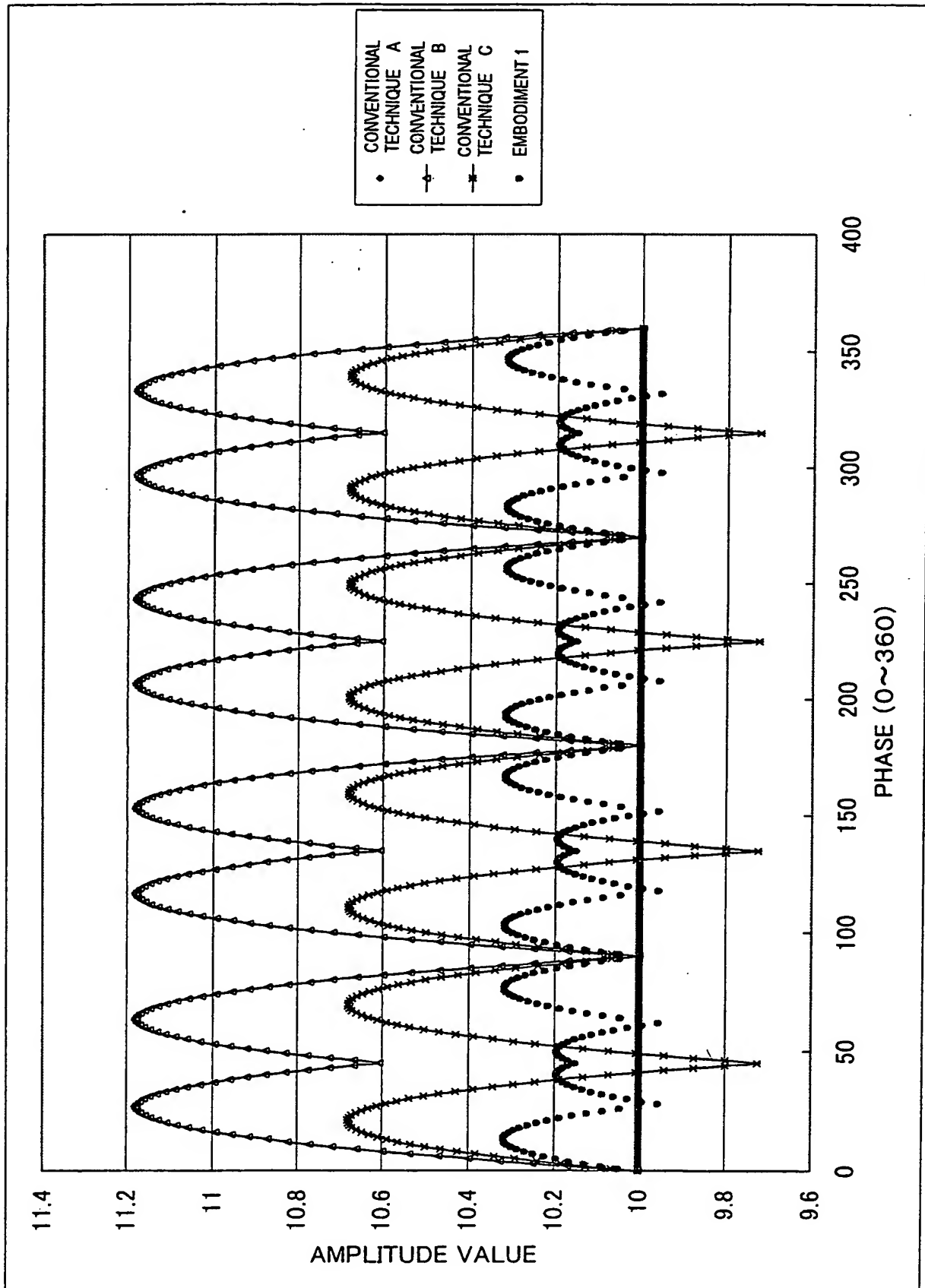


FIG.12

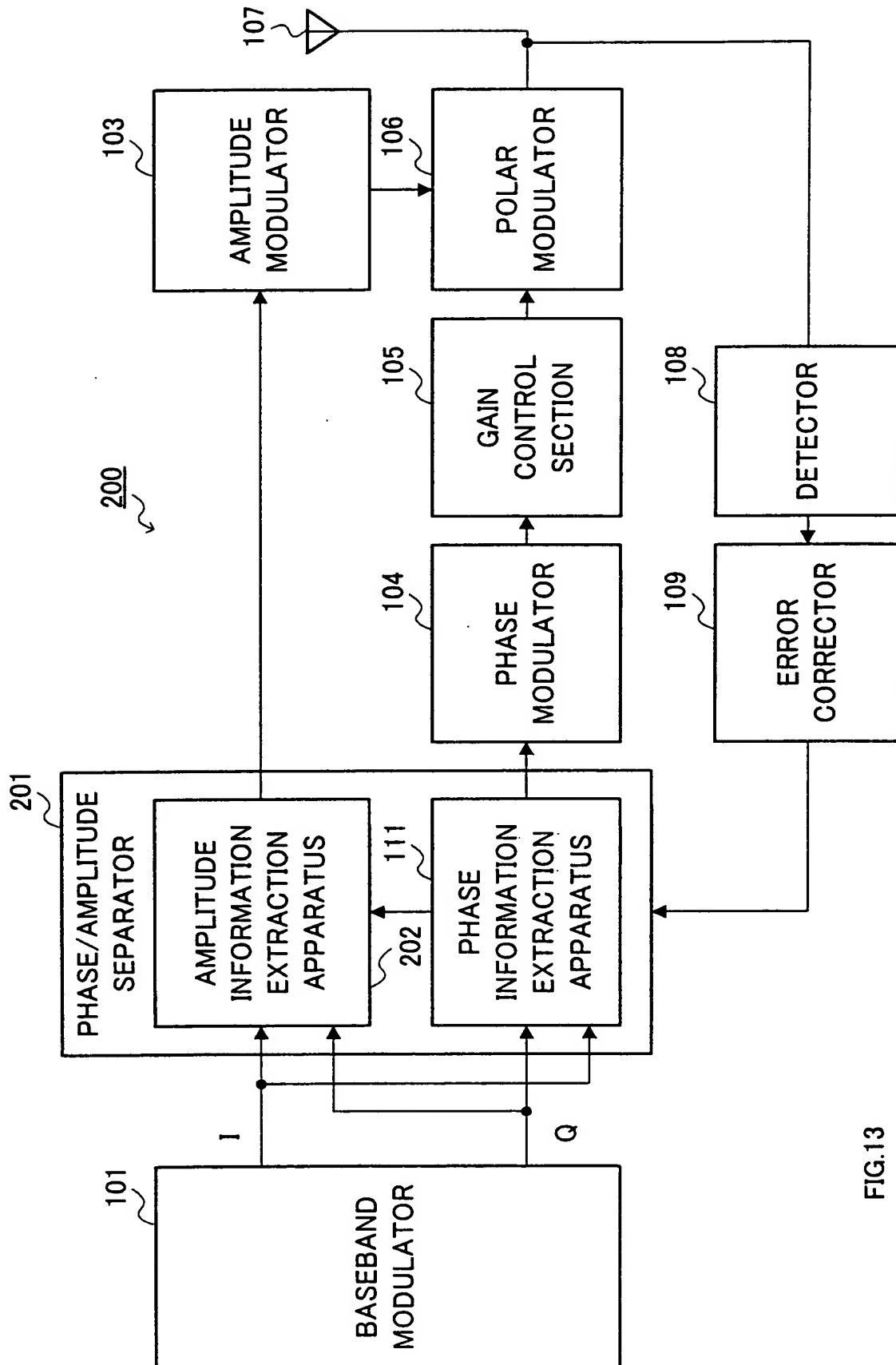


FIG.13

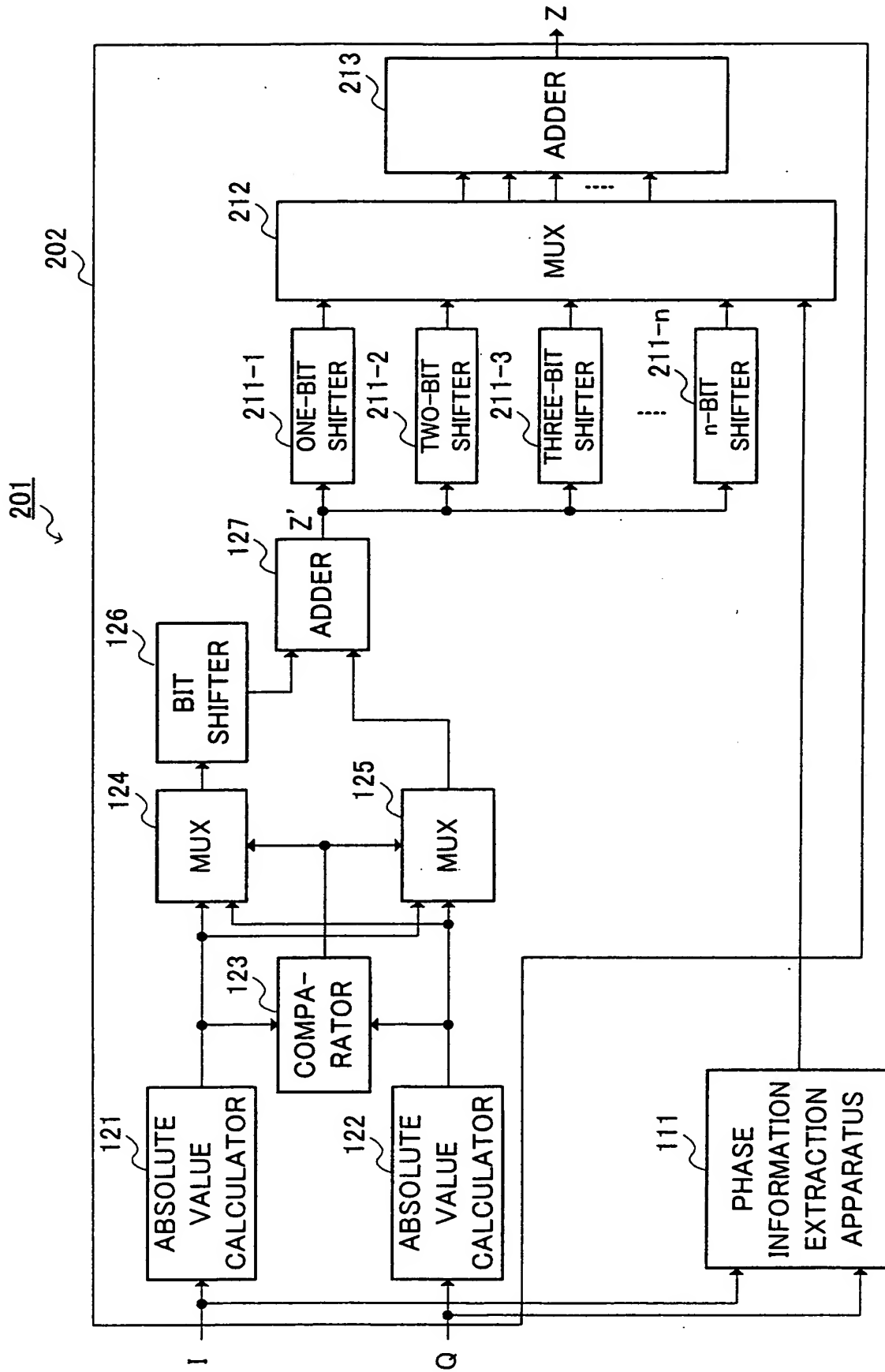


FIG.14

PHASE	ERROR RATE (Z=ERROR RATE x Z')	BIT SHIFT ADDITION VALUE
0	1	0
1	0.9921875	1+2+3+4+5+6+7
2	0.984375	1+2+3+4+5+6
3	0.9765625	1+2+3+4+5+7
4	0.96875	1+2+3+4+5
5	0.9609375	1+2+3+4+6+7
6	0.95703125	1+2+3+4+6+8
7	0.94921875	1+2+3+4+7+8
8	0.94140625	1+2+3+4+8
9	0.9375	1+2+3+4
10	0.93359375	1+2+3+4-8
11	0.9296875	1+2+3+4-7
12	0.921875	1+2+3+4-6
13	0.91796875	1+2+3+5+7+8
14	0.91796875	1+2+3+5+7+8
15	0.9140625	1+2+3+5+7
16	0.91015625	1+2+3+5+8
17	0.90625	1+2+3+5
18	0.90625	1+2+3+5
19	0.90234375	1+2+3+6+7+8
20	0.8984375	1+2+3+6+7
21	0.8984375	1+2+3+6+7
22	0.8984375	1+2+3+6+7
23	0.89453125	1+2+3+6+8
24	0.89453125	1+2+3+6+8
25	0.89453125	1+2+3+6+8
26	0.89453125	1+2+3+6+8
27	0.89453125	1+2+3+6+8
28	0.89453125	1+2+3+6+8
29	0.89453125	1+2+3+6+8
30	0.89453125	1+2+3+6+8
31	0.8984375	1+2+3+6+7
32	0.8984375	1+2+3+6+7
33	0.8984375	1+2+3+6+7
34	0.90234375	1+2+3+6+7+8
35	0.90625	1+2+3+5
36	0.90625	1+2+3+5
37	0.91015625	1+2+3+5+8
38	0.9140625	1+2+3+5+7
39	0.91796875	1+2+3+5+7+8
40	0.91796875	1+2+3+5+7+8
41	0.921875	1+2+3+4-6
42	0.9296875	1+2+3+4-7
43	0.93359375	1+2+3+4-8
44	0.9375	1+2+3+4
45	0.94140625	1+2+3+4+8

FIG.15

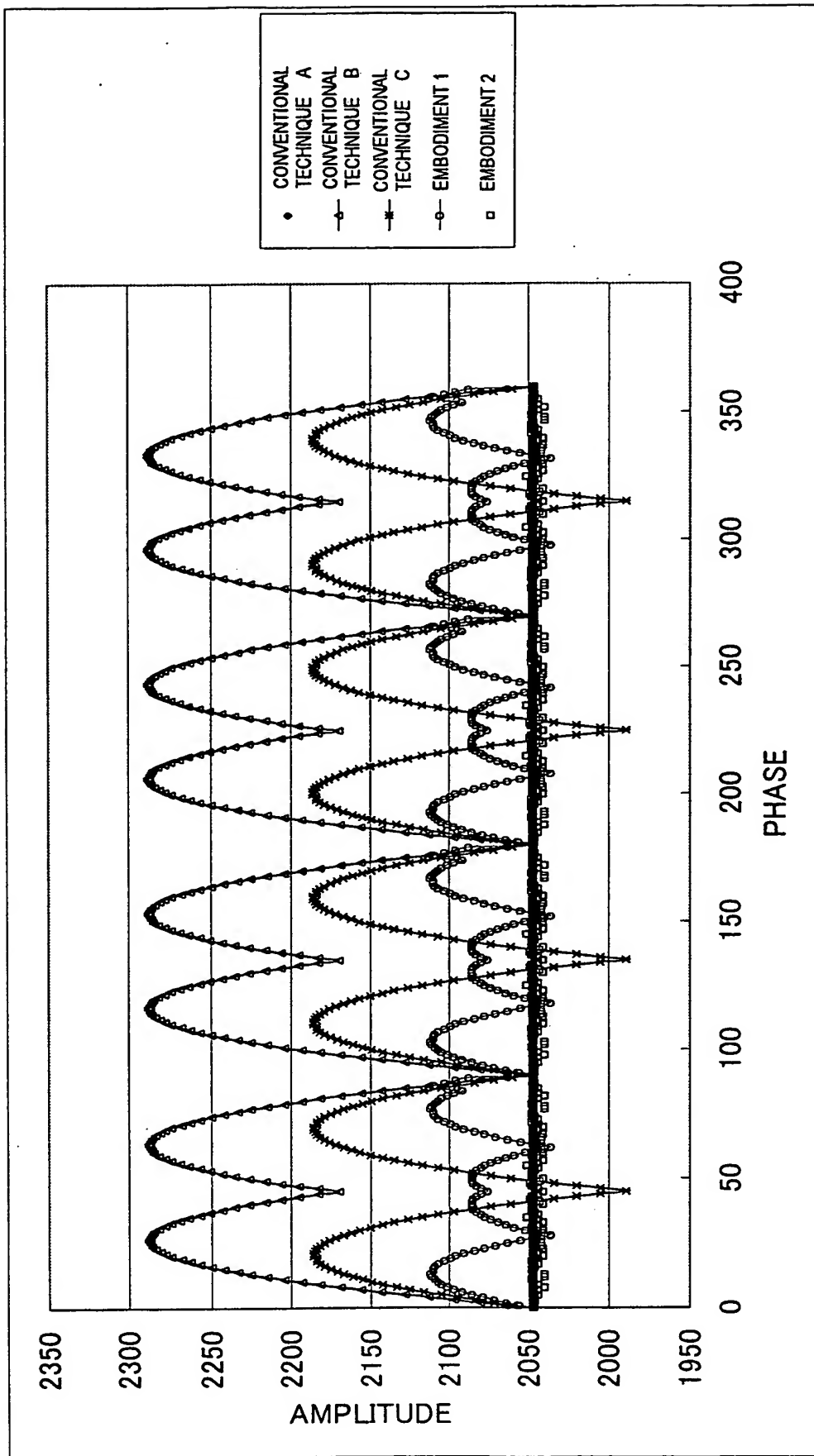


FIG.16